What is claimed is:

1. A plasma display panel comprising:

a plurality of first barriers successively formed on a substrate at constant intervals;

a plurality of first sustain electrodes successively formed at a width more than 40% of a pixel pitch, which is an overall distance of four of the first barriers, to be orthogonal to the first barriers;

a plurality of second sustain electrodes spaced apart from the first sustain electrodes at a distance less than 20% of the pixel pitch and mated with the first sustain electrodes one by one; and

a dielectric layer formed at a thickness of 25μm or more to cover the first and second sustain electrodes.

2. The plasma display panel as claimed in claim 1, wherein the first sustain electrodes and the second sustain electrodes are alternately formed.

3. The plasma display panel as claimed in claim 1, wherein one of the first sustain electrodes and one of the second sustain electrodes constitute a pair, and a pair of the sustain electrodes are arranged in different positions from the sustain electrodes of adjacent pairs.

4. The plasma display panel as claimed in claim 1, further comprising a plurality of second barriers successively formed between the respective first barriers to be orthogonal to the first barriers.

5. The plasma display panel as claimed in claim 1, wherein the first sustain electrodes have the same width as that of the second sustain electrodes.

6. A plasma display panel comprising:

a plurality of first barriers successively formed on a substrate at predetermined intervals;

a plurality of first sustain electrodes formed at a width more than 40% of a pixel pitch, which is an overall distance of four of the barriers, to be orthogonal to the first barriers;

a plurality of second sustain electrodes spaced apart from the first sustain electrodes at a distance less than 20% of the pixel pitch and mated with the first sustain electrodes one by one;

a dielectric layer formed at a thickness of 25μm or more to cover the first and second sustain electrodes; and

a plurality of conductive materials formed on some of the dielectric layer corresponding to the first sustain electrodes and the second sustain electrodes in each discharge cell.

7. The plasma display panel as claimed in claim 6, wherein the first sustain electrodes and the second sustain electrodes are alternately formed.

8. The plasma display panel as claimed in claim 6, wherein one of the first sustain electrodes and one of the second sustain electrodes constitute a pair, and a pair of the sustain electrodes are arranged in different positions from the sustain electrodes of adjacent pairs.

9. The plasma display panel as claimed in claim 6, further comprising a plurality of second barriers successively formed to be orthogonal to the first barriers so that the first and second sustain electrodes are arranged in pairs.

10. The plasma display panel as claimed in claim 6, wherein the first sustain electrode and the second sustain electrode are of a transparent electrode having a predetermined width and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

11. The plasma display panel as claimed in claim 10, wherein the conductive material is formed in some portion on the dielectric layer corresponding to the metal electrode.

12. The plasma display panel as claimed in claim 6, wherein

the first sustain electrode and the second sustain electrode have the same width as each other.

13. The plasma display panel as claimed in claim 6, wherein the conductive material is of metal or transparent electrode.

14. A plasma display panel comprising:

a plurality of first barriers successively formed on a predetermined substrate at predetermined intervals;

a plurality of first sustain electrodes formed at a width more than 40% of a pixel pitch, which is an overall distance of four of the barriers, to be orthogonal to the first barriers;

a plurality of second sustain electrodes spaced apart from the first sustain electrodes at a distance less than 20% of the pixel pitch and mated with the first sustain electrodes one by one;

a dielectric layer formed at a thickness of 25μm or more to cover the first and second sustain electrodes; and

a plurality of pads of a conductive material formed on some of the dielectric layer corresponding to the respective sustain electrodes, having different sizes for discharge cells displaying red, green and blue.

15. The plasma display panel as claimed in claim 14, wherein the first sustain electrodes and the second sustain electrodes are alternately formed.

16. The plasma display panel as claimed in claim 14, wherein one of the first sustain electrodes and one of the second sustain electrodes constitute a pair, and a pair of the sustain electrodes are arranged in different positions from the sustain electrodes of adjacent pairs.

17. The plasma display panel as claimed in claim 14, further comprising a plurality of second barriers successively formed to be orthogonal to the first barriers so that the first and second sustain electrodes are arranged in pairs.

18. The plasma display panel as claimed in claim 14, wherein the first sustain electrode and the second sustain electrode are of a transparent electrode having a predetermined width and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

19. The plasma display panel as claimed in claim 18, wherein the respective pad is formed in some portion on the dielectric layer corresponding to the metal electrode.

20. The plasma display panel as claimed in claim 14, wherein the conductive material is of metal or transparent electrode.

21. The plasma display panel as claimed in claim 14, wherein the pad includes a first pad formed with a predetermined width

in a discharge cell displaying green, a second pad formed with a width of 110% to 130% of the first pad in a discharge cell displaying red, and a third pad formed with a width of 70% to 90% in a discharge cell displaying blue.